



DFW

Docket No.: 060188-0817

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
OHNISHI, TERUHITO, et al. : Confirmation Number: 3828
Application No.: 10/807,307 : Group Art Unit: 2822
Filed: March 24, 2004 : Examiner: LEWIS, Monica
For: BIPOLAR TRANSISTOR AND METHOD FOR FABRICATING THE SAME

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Restriction Requirement
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Noting the Office Action of August 25, 2005 wherein restriction has been required, Applicant hereby elects Group II (claims 7-11), drawn to a process for fabricating a bipolar transistor, for prosecution in the above-identified application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

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Date: September 14, 2005